

<b>Notice of References Cited</b>	Application/Control No. 10/614,970	Applicant(s)/Patent Under Reexamination ALSUP ET AL.	
	Examiner Benjamin P. Geib	Art Unit 2181	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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*	B	US-5,630,083	05-1997	Carbine et al.	712/212
*	C	US-6,260,138	07-2001	Harris, Jeremy G	712/239
*	D	US-2004/0049657	03-2004	Kling, Ralph M.	712/208
*	E	US-6,012,125	01-2000	Tran, Thang M.	711/125
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*	G	US-5,634,108	05-1997	Freeman, Richard D.	711/118
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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	"Trace Cache: a Low Latency Approach to High Bandwidth Instruction Fetching"; Eric Rotenberg, Steve Bennett, and James E. Smith; 1996 IEEE
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.